

## REMARKS

Reexamination and reconsideration of the application as amended are requested.

A corrected IDS is enclosed as required by the examiner, including changing the IDS to denote that it is a one page IDS.

The examiner's objection to the drawings is respectfully traversed in view of the enclosed replacement sheet for figures 1 and 2. The specification has been amended to delete the reference characters which have been deleted from figure 1.

The examiner's rejection of claims 5, 11 and 17, under 35 USC 112, as being non-enabling is respectfully traversed. The examiner alleges that the wording "during operation of the Flash serial input transmission line, the Flash serial input transmission line transmits a command selected from the group consisting of a status command, a read command, and a write command" are non-enabling. The examiner alleges the CLK, SI, SO, CS, and RESET are commands forming a 5 bit binary number and that the Flash serial input transmission line cannot select a command from the group consisting of a status command, read command, and write command. Applicant disagrees. An example of a read command transmitted by the Flash serial input transmission line is a series of specific 1's and 0's which specify that data is to be read from a specific address or addresses in memory, as is well known to those skilled in the art. An example of a write command transmitted by the Flash serial input transmission line is a series of different specific 1's and 0's which specify that data is to be written to a specific address or addresses in memory, as is well known to those skilled in the art. Obviously, the write command can be transmitted, but the Flash to ROM interface does not pass through the write command to the ROM memory cells (see claims 6, 12 and 18). An example of a status command is series of yet different specific 1's and 0's, as is well known to those skilled in the art. A description of the operation of a Flash serial input transmission line for a Flash memory is found in a description of the Atmel AT45DB011 Serial DataFlash (cited in the enclosed Supplemental IDS). In the Atmel reference, see the block diagram on page 2, the description of read, buffer write, and status

register on pages 3-6, and especially note, for example, the command opcode for the SI signal on pages 12 and 13.

The examiner's rejection of claim 13, under 35 USC 112, as being non-enabling is respectfully traversed. The examiner states that the disclosure does not give sufficient detail into the operation of the ROM programmer and that a ROM (as defined by applicant) cannot be programmed. Nothing in claim 13 or the specification describes the ROM as being programmed, and nothing in claim 13 or the specification mentions any ROM programmer. What claim 13 describes is "storing the final version of the computer code in ... ROM ...". The word "storing" when used to describe storing a computer code in ROM is understood by those skilled in the art to be a valid statement and to be within the level of skill of the artisan. For example, claim 1 of US Patent No. 5,499,295 (cited in the enclosed Supplemental IDS) assigned to Ericsson, Inc. describes "... an identification storage device for storing an identifying value ...", wherein claim 3 states "... said identification storage device is a non-programmable read only memory." "Storing" does not have to mean "programming" or "writing". "Storing" includes the examiner's using diodes and no diodes to build a computer code into a basic ROM.

The examiner's rejection of claims 1-5 as being anticipated, under 35 USC 102, is respectfully traversed. The examiner rejects these claims as being unpatentable over Ali (6,016,472). Claims 2-5 depend from claim 1. Claim 1 now requires a non-ROM to ROM interface operatively connected to the ROM memory cells without any interposed RAM. Ali interposes a RAM between the operative connection of his interface 122 and his ROM 312.

The examiner's rejection of claims 7-11 as being obvious, under 35 USC 103, is respectfully traversed. The examiner rejects these claims as being unpatentable over Nojima (6,250,827) in view of Ali. Claims 8-11 depend from claim 7. Claim 7 now requires a non-ROM to ROM interface operatively connected to the ROM memory cells without any interposed RAM. Ali interposes a RAM between the operative connection of his interface 122 and his ROM 312. The examiner has alleged it would have been obvious to combine Nojima and Ali to arrive at applicant's claims 7-11 stating that the motivation to do so would be to allow easy debugging and changes to the internal program memory of the print ASIC through the Flash

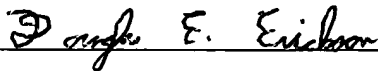
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Attorney Docket No.: 2001-0273.00  
Amendment

Memory. There is nothing in Nojima and/or Ali which teaches, suggests or describes such motivation.

The examiner's rejection of claims 13-17 as being obvious, under 35 USC 103, is respectfully traversed. The examiner rejects these claims as being unpatentable over Nojima (6,250,827) in view of Ali. Claims 14-17 depend from claim 13. Claim 13 now requires that the non-ROM to ROM interface is operatively connected to the ROM memory cells of the second memory module without any interposed RAM. Ali interposes a RAM between the operative connection of his interface 122 and his ROM 312. The examiner has alleged it would have been obvious to combine Nojima and Ali to arrive at applicant's claims 13-17 stating that the motivation to do so would be to allow easy debugging and changes to the internal program memory of the print ASIC through the Flash Memory. There is nothing in Nojima and/or Ali which teaches, suggests or describes such motivation.

Inasmuch as each of the objections and rejections has been answered by the above remarks, enclosed corrected IDS, enclosed replacement sheet of drawings, and amended specification and claims, it is respectfully requested that the objections and rejections be withdrawn, and that this application be passed to issue.

Respectfully submitted,



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**Amendments to the Drawings:**

A single sheet of formal drawings (labeled Replacement Sheet) for Figures 1 and 2 is enclosed, wherein Figure 1 has been amended to delete reference characters 14, 16, 36 and 38.